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Attorney's Docket No.: 07977/265001/US4590

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

The undersigned apologizes for the error in not providing a listing of claims previously. However, the basis for the restriction was not entirely understood.

The restriction is between FIGs. 1 and 12. The undersigned believes that this is a restriction between structure with a light emitting element, and structure with a liquid crystal layer. Assuming that to be the case, claim 1 reads on the elected species along with all claims that depend therefrom. Claims 11-16 read on the non-elected species of Embodiment II.

Claim 17 has been amended herewith into generic form, and hence is generic to both species. Claims 18-24 are similarly generic. New claims 37 and 38 are added herein, with claim 37 being specific to the elected species of Embodiment I, and claim 38 being directed to the non-elected species of Embodiment II.

Claims 25-29 are directed to the elected species. Claims 30-36 are directed to the non-elected Embodiment II.

Claims 39-46 are also added herein. Claim 39 is generic to both species along with claims 42-46, which depend therefrom. Claim 40 is directed specifically to the Embodiment of FIG. 1 and claim 41 is directed specifically to the non-elected Embodiment of FIG. 12.

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Since generic claims are present, the Examiner is asked to maintain the non-elected claims as "withdrawn from consideration", but is requested to examine these non-elected claims at such time as the generic claims are indicated as allowable.

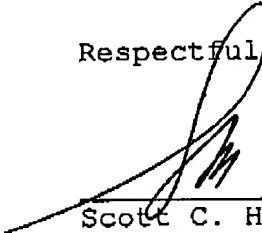
In view of the above amendments and remarks, therefore, all of the claims should be in condition for allowance. A formal notice to that effect is respectfully solicited.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

8/13/02

  
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VERSION TO SHOW CHANGES MADE

In the Claims:

Claims 1, 3-6, 11-14, 17-20, and 22 have been amended as follows:

1. (Amended) A semiconductor device comprising:

color filters on a substrate;

an adhesive layer on [a] the substrate and the color

filters;

an insulating film on said adhesive layer;

a plurality of TFTs over the insulating film and the color

filters; and

light emitting elements on [said insulating film] the TFTs,

wherein light emitted from said light emitting elements is emitted through said substrate.

3. (Amended) A device according to claim 1, wherein said semiconductor device further comprises driver circuits [on] over said insulating film and the color filters, and [said light emitting elements and] said driver circuits comprise TFTs.

4. (Amended) A device according to claim 1, wherein color filters are provided on said substrate at positions aligned with the TFTs [said light emitting elements].

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5. (Amended) A device according to claim [4] 1, wherein said insulating film covers said color filters, and [is] has a planarized surface.

6. (Amended) A device according to claim [4] 1, wherein red color filters of said color filters are provided at positions aligned with at least [the] channel forming regions of said TFTs.

11. (Amended) A semiconductor device comprising:

[a first substrate comprising an organic material and having TFTs provided thereon;

a second substrate; an

a liquid crystal material retained between said first and second substrates,

wherein color filters are provided between said first substrate and said TFTs]

color filters on a substrate;

an adhesive layer on the substrate and the color filters;

an insulating film on said adhesive layer;

a plurality of TFTs over the insulating film and the color filters; and

a liquid crystal layer over the TFTs.

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12. (Amended) A device according to claim 11, wherein said [first] substrate [comprising an organic material] is a plastic substrate.

13. (Amended) A device according to claim 11, [further comprising an insulating film covering said color filters and] wherein said insulating film has a planarized surface.

14. (Amended) A device according to claim 11, wherein said color filters are provided at positions aligned with at least [the] channel forming regions of said TFTs.

17. (Amended) A semiconductor device comprising:  
color filters [are] provided on a substrate;  
an adhesive layer over said substrate and said color filters;  
an insulating film on said adhesive layer; and  
a plurality of TFTs over the insulating film and the color filters

[light emitting elements on said insulating film,  
wherein emitted from said light emitting elements is  
emitted through said substrate, and

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wherein said substrate is a plastic substrate comprising an organic material].

18. (Amended) A device according to claim 17, wherein said semiconductor device further comprises driver circuits [on] over said insulating film and said color filters, and [said light emitting elements and] said driver circuits comprise TFTs.

19. (Amended) A device according to claim 17, wherein [at positions aligned with said light emitting elements] the substrate comprises organic material.

20. (Amended) A device according to claim 17, wherein red color filters of said color filters are provided at positions aligned with at least [the] channel forming regions of said TFTs.

22. (Amended) A device according to claim 17, wherein a fixing substrate is provided over said [light emitting element] TFTs so as to face said substrate.

New claims 37-46 have been added.